

FORM PTO-1449

Docket Number (Optional)

Application Number

YOR9200010217US1

10/052,591

Applicant

Furman et al.

Filing Date

January 18, 2002

Group Art Unit

Not yet assigned

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

APR 23 2002

(Use several sheets if necessary)

## U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
KS	6,339,024	1/15/02	Petrarca et al.	438	686	
KS	6,344,125	2/5/02	Locke et al.	205	118	
KS	6,368,484	4/9/02	Volant et al.	205	220	

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

## OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

KS	Richard Volant, Kevin Petrarca, Peter Locke, James A. Tornello and Donald F. Canaperi, Title: "Replated Metal Structures for Semiconductor Devices", U.S. Patent application Serial No. 09/657,469, filed on May 9, 2000.

EXAMINER

*Wiley Hester*

DATE CONSIDERED

8-18-03

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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